

06/28/00
JUL 12 U.S. PTO

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L. L. P.

1300 I STREET, N. W.
WASHINGTON, DC 20005-3315

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09/604724
06/28/00

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404 • 653 • 6400
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650 • 849 • 6600

WRITER'S DIRECT DIAL NUMBER:

(202) 408-4024

June 28, 2000

TOKYO
011 • 813 • 3431 • 6943
BRUSSELS
011 • 322 • 646 • 0353

ATTORNEY DOCKET NO.: 04329.2335

Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231



22852

PATENT TRADEMARK OFFICE

New U.S. Patent Application
Title: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE
Inventors and Addresses:

- 1) Tokuhiisa OHIWA
Kawasaki-shi, Japan
- 2) Shoji SETA
Yokohama-shi, Japan
- 3) Nobuo HAYASAKA
Yokosuka-shi, Japan
- 4) Katsuya OKUMURA
Yokohama-shi, Japan
- 5) Akihiro KOJIMA
Yokohama-shi, Japan
- 6) Junko OHUCHI
Yokohama-shi, Japan
- 7) Tsukasa AZUMA
Tokyo, Japan
- 8) Hideo ICHINOSE
Yokohama-shi, Japan
- 9) Ichiro MIZUSHIMA
Yokohama-shi, Japan

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

Assistant Commissioner for Patents

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1. A check for \$768.00 representing the filing fee.
2. Application - 32 pages, including 4 independent claims and 20 claims total. ✓
3. Drawings - 5 sheets of formal drawings containing 29 figures.
4. Information Disclosure Statement and Information Disclosure Citation, PTO 1449 with document attached.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicants claim the right to priority based on Japanese Patent Application No. 11-183908, filed on June 29, 1999.


Please accord this application a serial number and filing date.

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or § 1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By:


Richard V. Burgujian
Reg. No. 31,744

RVB/FPD/sci
Enclosures

003230-4240950